

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10014243	FILING DATE 12/11/2001	CLASS 257-	SUBCLASS 204	GAU 2811	EXAMINER MALLON/HCP/HY
----------------------	---------------------------	---------------	-----------------	-------------	---------------------------

SC638
**APPLICANTS: Van Den Heuvel Renerus; 2823

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 00204429.5 12/11/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO NL 000674
TITLE : Method for the manufacture of a semiconductor device with a field-effect transistor U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claims for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawn.	Figs. Drawn.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		Application Examiner		
PREPARED FOR ISSUE				
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)